

## LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	11/6	ES definition, ES vs General Computing System	I	White Board		
		History of Embedded Systems	"	"		
2	12/6	Classification of Embedded Systems	"	"		
		Major Application Areas of ES	"	"		
3	12/6	Purpose of Embedded System	"	"		
		Typical ES	"	"		
4	15/6	Core of the ES	"	"		
		memory	"	"		
5	18/6	Sensors	"	"		
		Actuators	"	"		
6	19/6	Communicating Interfaces	"	"		
7	19/6	Embedded Firmware	"	"		
8	22/6	Other System components	"	"		
		PCB & Passive components	"	"		
9	18/6	Characteristics of an ES	II	"		
10	19/6	Application specific ES - NM	"	"		
11	19/6	Domain specific ex. of ES	"	"		
12	23/6	Automotive	"	"		
13	25/6	Analog & Digital electronic components	III	"		
			"	"		

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14	<del>29/6</del> 13	I/O Types	III	BB		
		I/O examples	"	"		
15	<del>29/6</del> 13	Serial Communication Devices	"	"		
16	<del>29/6</del> 13	} parallel device ports	"	"		
			"	"		
17	<del>29/6</del> 13	Wireless Devices	"	"		
18	<del>3/7</del> 13	Timer devices	"	"		
19	<del>3/7</del> 13	} Counting devices	"	"		
			"	"		
20	<del>6/7</del> 13	Watchdog timer	"	"		
21	<del>9/7</del> 13	Real Time clock	"	"		
22	<del>10/7</del> 13	VLSI design	"	"		
23	<del>10/7</del> 13	} Integrated circuit design	"	"		
			"	"		
24	<del>16/7</del> 13	EDA Tools	"	"		
25	<del>17/7</del> 13	ORCAD EDA Tool	"	"		
26	<del>17/7</del> 13	} The PCB - Layout - Design	"	"		
27	<del>20/7</del> 13		"	"		
			"	"		

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28	<del>23/7</del> 13	Embedded firm ware design approach	IV	"		
	<del>23/7</del> 13	Embedded firm ware development	"	"		
29	<del>24/7</del> 13	Languages	"	"		
30	<del>24/7</del> 13	ISR concept	"	"		
	<del>24/7</del> 13	Interrupt Sources	"	"		
31	<del>27/7</del> 13	Interrupt servicing	"	"		
	<del>27/7</del> 13	mechanisms	"	"		
32	<del>30/7</del> 13	multiple Interrupts	"	"		
33	<del>31/7</del> 13	DMA, Device drive	"	"		
34	<del>31/7</del> 13	programming	"	"		
35	<del>3/8</del> 13	Concept of C vs Embedded C	"	"		
	<del>3/8</del> 13	Compiler Vs cross Compiler	"	"		
36	<del>13/8</del> 13	operating system Basics	V	"		
37	<del>14/8</del> 13	Types of OS	"	"		
38	<del>14/8</del> 13	Tasks, processes & Threads	"	"		
39	<del>14/8</del> 13	processes and Scheduling	"	"		
40	<del>20/8</del> 13	Task communication	"	"		
41	<del>21/8</del> 13	Task synchronization	"	"		
42	<del>21/8</del> 13	Device drivers	"	"		
43	<del>24/8</del> 13	How to use an RTOS	"	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
44	<del>2/12</del> 13	Fundamental Issues in H/W & S/W Co-design	<u>VI</u>	"		
	<del>1/1</del>		"	"		
45	<del>2/12</del> 13	Computational models in Embedded design	"	"		
	<del>1/1</del>		"	"		
46	<del>2/12</del> 13	H/W & S/W Trade-offs	"	"		
	<del>1/1</del>		"	"		
47	<del>3/12</del> 13	Integration of Hardware & Firmware	"	"		
	<del>1/1</del>		"	"		
48	<del>4/12</del> 13	Firmware	"	"		
	<del>1/1</del>		"	"		
49	<del>4/12</del> 13	I/O	"	"		
	<del>1/1</del>		"	"		
50	<del>5/12</del> 13	Issues in Embedded Systems Design	"	"		
	<del>1/1</del>		"	"		
51	<del>6/12</del> 13	The Integrated development Environment	<u>VII</u>	"		
	<del>1/1</del>		"	"		
52	<del>11/12</del> 13	Types of files generated on cross compilation	"	"		
	<del>2/1</del> 13		"	"		
53	<del>11/12</del> 13	Disassembler/Decompiler	"	"		
54	<del>12/12</del> 13	Simulators & Emulators	"	"		
55	<del>18/12</del> 13	Debugging, Target H/W debugging	"	"		



## LESSON PLAN

[illegible]

Write lesson plan  
according to  
JNTU sheet